Senior Digital IC Design Front-End Engineer

About the company

QPlox Engineering is a leading technology company specializing in innovative solutions for IoT frameworks, instrumentation systems, and test automation. Our clients are major multinational enterprises and local companies from automotive, semiconductors, RF, and consumer electronics. We are committed to pushing the boundaries of technology and providing our clients with cutting-edge solutions. Our dynamic team thrives on collaboration, creativity, and continuous learning.

Job Description

We are seeking a **Senior Digital IC Design Engineer (Front-End)** for a long-term position to contribute to the development of next-generation RF chips. This is an exciting opportunity to be at the forefront of innovation in ultra-high-speed digital design.

Key Responsibilities

- Integrate existing control and data path designs into new products.
- Update and verify legacy designs to align with new product requirements.
- Design and verify cutting-edge ultra-high-speed DSP blocks for next-generation products.
- Collaborate closely with system engineers to understand specifications and optimize power trade-offs.
- Work with the physical implementation team to enhance design methodologies and flows.
- This role requires technical hands-on expertise, and strong team collaboration. The ideal candidate should aim for excellence in technical performance, product quality, and project execution.

Location: Leuven, Belgium

Candidate Qualifications & Experience

- **MSEE or equivalent**, with **3 to 5 years of industrial experience** in digital front-end design.
- Strong knowledge of **Verilog and SystemVerilog** for both design and verification.
- Experience with **RTL coding, linting, and design verification**.
- Understanding of **DFT, ATPG, OCC, and CDC issues**.
- Familiarity with **UPF techniques** for low-power design.
- Experience with **delay-annotated gate-level simulations**.
- Proficiency in **simulation methodologies**, including **regression testing, UVM, functional coverage, and assertions**.

- Experience in **high-speed pipelined FIR DSP structures** is a plus.
- Solid grasp of simulation concepts such as regression testing, UVM, functional coverage, assertions, …
- Experience with implementation of high-speed pipelined FIR DSP structures is a plus
- Formal Verification experience is a plus

    a) Formal linting
    b) Sequential equivalence checking
    c) Assertion-based verification

- Strong experience with **C/C++/SystemC models** for RTL verification.
- **Python and TCL programming skills** are a strong advantage.
- Comfortable working in a **Linux command-line environment** and using **MS Office** for documentation and reporting.

Key Traits We Look For:

- Excellent **analytical and problem-solving skills**.
- Strong **communication skills** and ability to work in a **team-oriented environment**.
- Willingness to **relocate to the Leuven (Belgium) region**.
- Results-driven, with a **continuous improvement mindset**.
- Attention to detail and a strong sense of **determination**.

Benefits

- Competitive salary and benefits package.
- Opportunities for professional growth and development.
- Collaborative and innovative work environment.
- Plenty of learning opportunities.

Contact

Send your CV and application letter detailing their experience and qualifications to jobs@qplox.com with the subject ¨**Senior Digital IC Design Front-End Engineer¨.**