



# CMOS Based Device Processing and Integration Engineer

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## About the company

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QPlox Engineering is a leading technology company specializing in innovative solutions for IoT frameworks, instrumentation systems, and test automation. Our clients are major multinational enterprises and local companies from automotive, semiconductors, RF, and consumer electronics. We are committed to pushing the boundaries of technology and providing our clients with cutting-edge solutions. Our dynamic team thrives on collaboration, creativity, and continuous learning.

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## Job Description

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Our client is dedicated to enriching people's lives through cutting-edge communication technologies. With deep expertise in the telecom sector, we aim to bridge the digital divide and empower individuals to seamlessly participate in the digital world, regardless of their location. Our vision is to establish a world-class research centre, fostering innovation and the development of next-generation technologies and platforms. To strengthen our research efforts, we are currently looking for a **CMOS based Processing and Integration Engineer** to join our team.

## Key Responsibilities

- Develop process integration schemes for advanced devices, such as NS, CFET, and 2D-material-based circuits.
- Oversee the development of critical process steps/modules in collaboration with internal and external research teams.
- Define, characterize, and optimize process steps/modules to achieve overall performance goals for devices and processes.
- Stay up to date with relevant scientific literature and industry trends, producing benchmark reports on state-of-the-art developments.
- Propose and implement integrated process flows aligned with industry best practices, ensuring manufacturability and yield optimization.
- The role may involve occasional travel for participation in international conferences.

Location: Leuven, Belgium

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## Candidate Requirement

- **Education:** Preferably a **PhD** in Materials Science, Physics, Chemistry, or a related technical discipline. Candidates with a **Master's degree** and demonstrated relevant experience will also be considered.
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- **Hands-on Experience:** Minimum **3 years (preferably 5+ years)** of hands-on experience in CMOS lab/fab process engineering, with expertise in common CMOS processes (CVD, photolithography, dry and wet etching, metallization, metrology, CMP, wafer bonding).
  - **Technical Knowledge:**
    1. In-depth understanding of CMOS processes and their interdependencies.
    2. Familiarity with device physics and materials such as Si, 2D materials, and Ge.
    3. Ability to comprehend and develop process integration flows while also executing hands-on process step/module development.
  - **Industry Expertise:**
    1. Strong knowledge of available market tools and the ability to define selection criteria for process equipment.
    2. Experience in technology transfer is a significant advantage.
  - **Languages:** Fluent in English (written and spoken) and knowledge of Dutch or Chinese is a plus.
  - **Preferred Attitude & Skills**
    1. Strong team player with excellent collaboration skills.
    2. Effective written and verbal communication abilities.
    3. Solid project management and progress reporting capabilities.
    4. Detail-oriented with the ability to maintain a high-level perspective when needed.
    5. Highly organized and structured work approach.
    6. Continuous improvement mindset with a passion for innovation.

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## Benefits

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- Competitive salary and benefits package.
- Opportunities for professional growth and development.
- Collaborative and innovative work environment.
- Flexible working hours and remote work options.
- Plenty of learning opportunities.

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## Contact

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Send your CV and application letter detailing their experience and qualifications to [jobs@qplox.com](mailto:jobs@qplox.com) with the subject "**CMOS Based Device Processing and Integration Engineer**".

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