

About the company

Qplox is a fast growing company offering test and automation engineering. Headquartered in Leuven, our clients are major multinational enterprises and local companies from automotive, semiconductors, RF, consumer electronics.... Qplox consultancy department offers services in (micro) electronics design and electronics software. Our Test automation group offers a one stop shop for design of test benches, system integration production and lab automation solutions.

Job Description

We are offering a senior layout engineer position enabling you to join the creation of the next generation mobile phone chips.

The Analog & RF IC Layout Engineer will be part of an experienced layout team. In close cooperation with his colleagues he will be in charge of the layout of high speed analog and RF circuits and he will take up a leading role in the floorplanning and optimization of the system on chip.

He is an expert user of Cadence and Mentor tool suites on nanometer RF CMOS and/or RF SOI technology.

The successful candidate will work closely with the RF designers. He/she will take into account the constraints from the designer and will go through iterations with the designer to further optimize the layout from a performance and area perspective.

RF circuit layout experience in the sub 10-GHz range is mandatory.

The layout engineer should have a good understanding of the different circuit topologies and their constraints for the layout implementation. Good understanding of the layout rules enables to optimize the implementation.

The senior layout engineer is also capable to perform extraction of parasitics and make the interpretation of these results in view of further optimization.

Candidate Description

- Industry Degree qualified (BS or MS EE degree)
 - Ample experience with the Cadence OA VirtuosoXL
 - Experience with RFIC layout (GHz range) in CMOS technology
 - FinFET experience is a major plus.
 - Automation for layout is a plus.
 - Good communication skills & team-player.
 - Process oriented, ability to structure the RF layout process
 - Continuous strive for improvement in circuits and process.
 - Detail oriented and determined
 - Relocation to Belgium, if applicable, is strongly recommended.
-

We offer

Today, we're seeking team players who get things done and share a passion for bringing new wireless technologies to the rapidly growing mobile market. Now is your chance to join the engineering team and develop new, world-leading products.

An attractive salary package with extra benefits. A high tech, multicultural and young ambient. A fast track in a growing company. Formation in multidisciplinary environment plenty of learning opportunities.

Contact

Send your CV and application letter to jobs@qplox.com with the subject "Test automation engineer"
