



Senior RF layout Engineer

About the company

Qplox specializes in product qualification and release. We offer consultancy and engineering services for test and lab automation, characterization, verification, validation and end of line test. For a wide range of products, and with special focus in High tech.

Our engineering department develops custom made test and automation systems, electronic prototypes and test benches. Our consultancy department offers our clients a complete solution for their high tech qualification needs. From RF, Photonics, semiconductors... our engineers can help in the qualification of all your advanced products.

Job Description

You will be a member of the qplox R&D team, and working for our customer in the region of Leuven and part of our customer transceiver chip developments.

- Analog IC Layout Engineer based in Leuven to work on layout for RF blocks as LNAs, inductors and switches using Cadence and Mentor tool suites on RF CMOS and SOI technology. The successful candidate will work closely with the RF designers and will create the layout based on the schematic coming from the designer and make sure it is LVS/DRC compliant. He/she will take into account the constraints from the designer and will go through iterations with the designer to further optimize the layout from a performance and area perspective. As a lot of the structures are repetitive in nature, the use of layout automation tools (analog routers, placement) is an essential part of the job. Experience creating a layout for RF circuits in the sub 10-GHz range is mandatory.

Candidate Description

- Industry Degree qualified (Bs or Ms degree)
- Ample experience with the Cadence suite of Layout tools is a must.
- Experience with RFIC layout (GHz range) in CMOS technology is a must.
- Experience with Cadence V6 (Virtuoso, VirtuosoXL) and the EMX layout extraction tool is a big plus.
- Skill automation for layout is a plus.
- As a lot of the structures are very repetitive and complex, experience with semi-automated router tools and digital layout tools is a plus.
- Good communication skills & team-player.
- Process oriented, ability to structure the RF layout process

Key Words

RF transceivers, 2G, 3G, 4G, LTE, wireless technologies, mobile, Back End, RF IC, Layout, Cadence, CMOS.

We offer

Your performance and growth is monitored, assessed and rewarded, you will have an individual development plan based on your competences and interests. We offer an attractive salary package with extra-legal benefits. A high tech, multicultural and young company with fast promotion and many learning possibilities in a growing, multidisciplinary company.

Contact

Send your CV to jobs@qplox.com or surf to www.qplox.com
