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## About the company

Qplox specializes in product qualification and release. We offer consultancy and engineering services for test and lab automation, characterization, verification, validation and end of line test. For a wide range of products, and with special focus in High tech.

Our engineering department develops custom made test and automation systems, electronic prototypes and test benches. Our consultancy department offers our clients a complete solution for their high tech qualification needs. From RF, Photonics, semiconductors... our engineers can help in the qualification of all your advanced products.

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## Job Description

- As a member of the qplox R&D team, and working for our customer in the region of Leuven, We are offering a senior design position enabling you to join the creation of the next generation mobile phone chips.
  - The ability to analytically reason on different circuit topologies, based on an overview of the state-of-the-art, select the right architecture and tweak the right parameters to meet stringent criteria for these circuits is a must.
  - The candidate will design (IC design), simulate, layout and test integrated RF circuits in the 100 MHz to 15 GHz range in advanced CMOS (or similar) technologies. Subsystem design areas (Tx) may encompass a CMOS integrated PPA, PA, RFDAC, envelope modulator, LO distribution and (Rx) LNAs, RF filters (bandpass), switches and duplexers for use in low power, highly integrated wireless RF transceivers or peripheral circuits.
  - He/she will also perform experimental verification and debugging, along with the integration of complex RF & Mixed Signal functions into system chips. Building accurate models based on layout extraction and 2D/3D modeling tools to match measurements with simulations is essential
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## Candidate Description

- PhD is preferred or MSEE with equivalent experience
  - Expertise in CMOS integrated (digitized or analog) Tx architectures, advanced Rx architectures, or the candidate should have several years of professional experience in analog CMOS IC design AND system modeling in e.g. matlab.
  - Product experience in any of these domains is a plus: Digital inspired RF transceivers, RF filters, RF duplexers, PA's, RF Frontends, envelope modulators, switches
  - Experience with Cadence Spectre, Virtuoso or other equivalent Agilent and/or Mentor tools is required.
  - Ability to technically coach other team members.
  - Good communication skills & team-player.
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## Key Words

RF transceivers, 2G, 3G, 4G, LTE, wireless technologies, mobile.

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## We offer

Your performance and growth is monitored, assessed and rewarded, you will have an individual development plan based on your competences and interests. We offer an attractive salary package with extra-legal benefits. A high tech, multicultural and young company with fast promotion and many learning possibilities in a growing, multidisciplinary company.

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## Contact

Send your CV to [jobs@qplox.com](mailto:jobs@qplox.com) or surf to [www.qplox.com](http://www.qplox.com)

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