EDA Engineer Thin Film Electronics

About the company

Qplox is a fast growing company offering test and automation engineering. Headquartered in Leuven, our clients are major multinational enterprises and local companies from automotive, semiconductors, RF, consumer electronics…. Our consultancy department offers services in RF, semiconductors and electronics design and test. Our Test automation group offers a one stop shop for design of test benches, system integration production and lab automation solutions.

Job Description

As a part of a world-renowned team, you will be involved in supporting analog and digital designers for thin-film electronic applications. You will be working in a high-tech environment on the flexible devices of the future together with a multi-disciplinary team and with our international customers.
Today, technology and designs are gaining maturity enabling a clear roadmap to realize worlds-first products.

Your main task will be the developing the CAD tools for emerging technologies (e.g. thin-film electronics). The development of robust designs in these emerging technologies requires that all the information coming from device modeling is embedded in a standard software interface.
All the main analyses (as DC, AC, TRAN, NOISE, MC, STB, ...) should be supported and also pcells (like passive components) should be realized.

Additionally, rule decks for LVS, DRC and parasitic extraction should be developed in collaboration with the technology experts. In general, for correctly performing all these tasks, it is crucial that you will be able to communicate with people having different backgrounds (ranging from device physics to IC designers). You will be challenged to close the loop by comparing the EDA environment and lab measurements.
Your responsibilities will include:

- Translate device models into CADENCE compact models (Spice, Verilog-A)
- Design and characterize in the lab test circuits to improve the properties of the model (transient, noise, ...)
- Make sure that all main circuit analysis are performed properly
- Realize and maintain the rule decks for DRC, LVS and parasitic extraction
- Check potential of new software releases and run small feasibility studies on practical cases

Candidate Description

You want to make a difference and you are looking for a challenge. You are self-driven and constantly exploring new opportunities. You benchmark your results with state of the art and explore new methodologies that go beyond today's solutions

- You have a Master degree or equivalent experience in Electronics Design Automation. A PhD is plus.
- Awareness of the CAD tool requirements (e.g. for facilitation the convergence of the solver or develop proper DRC/LVS tools)
- Knowledge of novel transistor technologies beyond Si-CMOS (e.g. metal-oxide transistors) is a strong plus.
- Solid experience with IC prototyping (design for testing and manufacturability, automatic test, debugging, etc.)
- Good team player with competent communication and reporting skills.
- Good knowledge of English is mandatory.

We offer

An attractive salary package with extra benefits. A high tech, multicultural and young ambient. A fast track in a growing company. Formation in multidisciplinary environment plenty of learning opportunities.

Contact

Send your CV and application letter to jobs@qplox.com with the subject "Test automation engineer"