
About the company

Qplox specializes in product qualification and release. We offer consultancy and engineering services for test and lab automation, characterization, verification, validation and end of line test. For a wide range of products, and with special focus in High tech.

Our engineering department develops custom made test and automation systems, electronic prototypes and test benches. Our consultancy department offers our clients a complete solution for their high tech qualification needs. From RF, Photonics, semiconductors... our engineers can help in the qualification of all your advanced products.

Job Description

- As a member of the qplox R&D team, and working for our customer in the region of Leuven, the digital verification engineer will take responsibility for the verification of a single IP or the (digital) top level of a transceiver chip, which includes analog/RF blocks, digital blocks and SW running on the processor.
- This will include the following tasks:
 - Creating a verification plan based on the chip/IP definition
 - Creating a testbench using industry proven verification methodologies with System Verilog and UVM
 - Executing the verification plan through directed or constrained random verification. Full automatic regression etc
 - Setting up and running gate level verification
 - Interacting with the mixed signal verification team to support them with their activities by reusing the digital testbench for mixed signal verification.

Candidate Description

- MSEE or equivalent.
- Candidate should have at least 3-5 years hands-on experience in design and verification of pure digital and/or mixed signal IPs or ICs.
- 5 years in IC development process
- 3 years working experience in IC design/verification.
- 1 to 3 years in audio/DSP processing architectures
- Experience in Systemverilog and UVM concepts and implementation or equivalent methods
- Setting up and executing gate level verification.
- Strong knowledge of functional and code coverage.
- Good root cause analysis skills
- Good communication skills & team-player.
- Detail oriented and determined
- Tool/Language knowledge: System Verilog, UVM, Incisive, Scripting skills

Key Words

Audio, DSP, verification, IC, IP, UVM, System Verilog, scripting, python.

We offer

Your performance and growth is monitored, assessed and rewarded, you will have an individual development plan based on your competences and interests. We offer an attractive salary package with extra-legal benefits. A high tech, multicultural and young company with fast promotion and many learning possibilities in a growing, multidisciplinary company.

Contact

Send your CV to jobs@qplox.com or surf to www.qplox.com
